FIG. 1

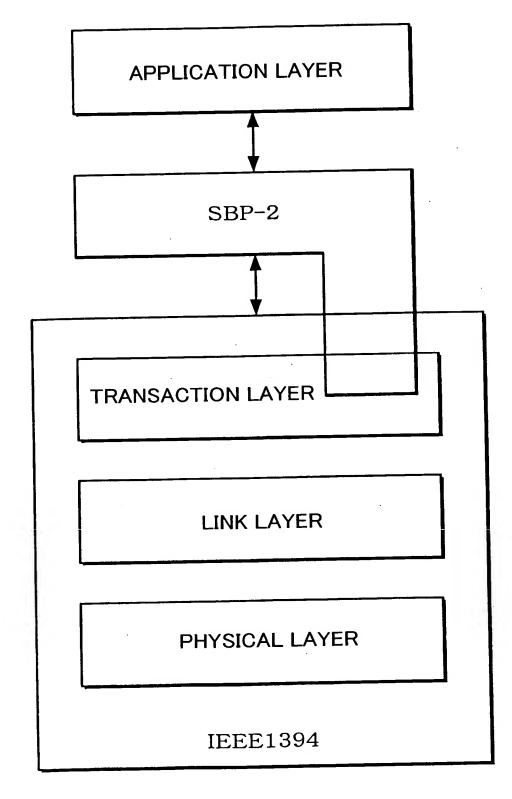


FIG. 2

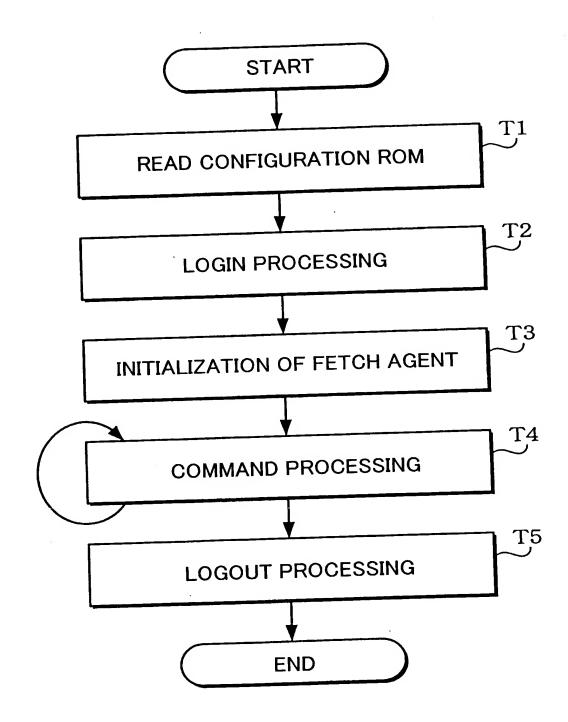


FIG. 3

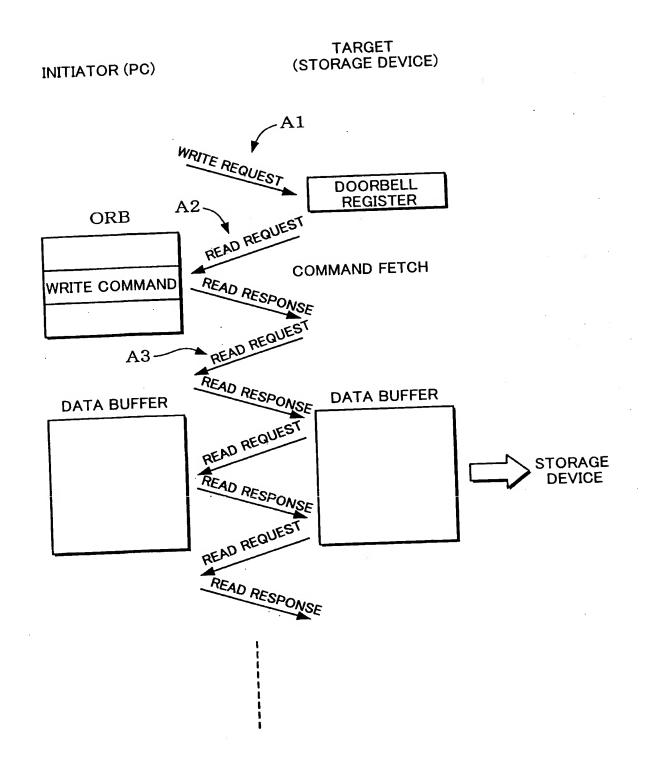


FIG. 4

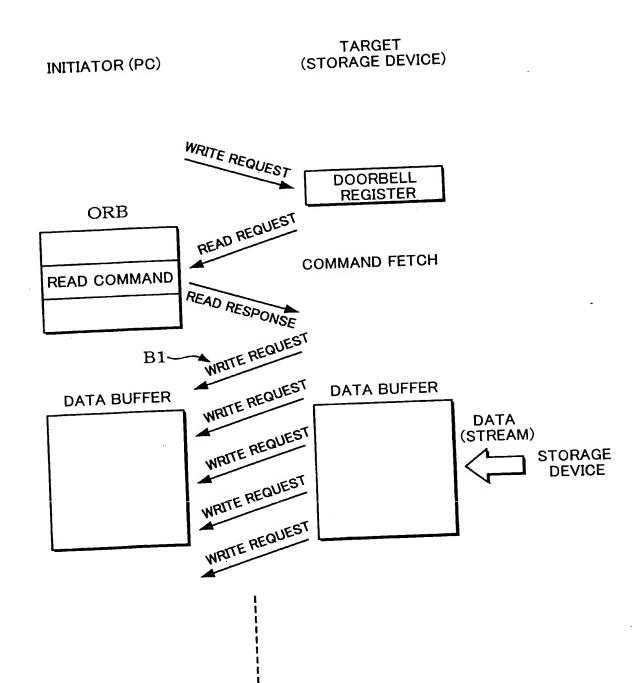


FIG. 5A

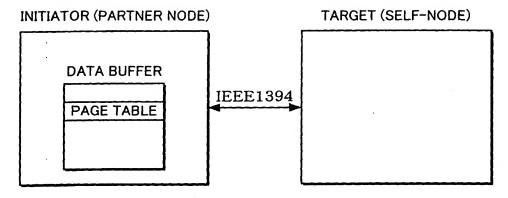


FIG. 5B PAGE TABLE IS PRESENT

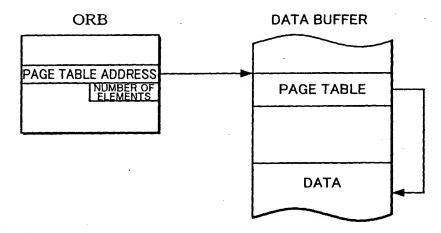
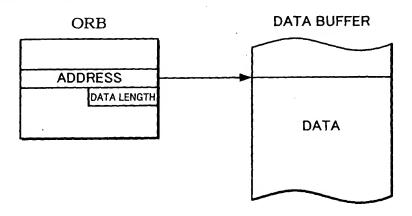
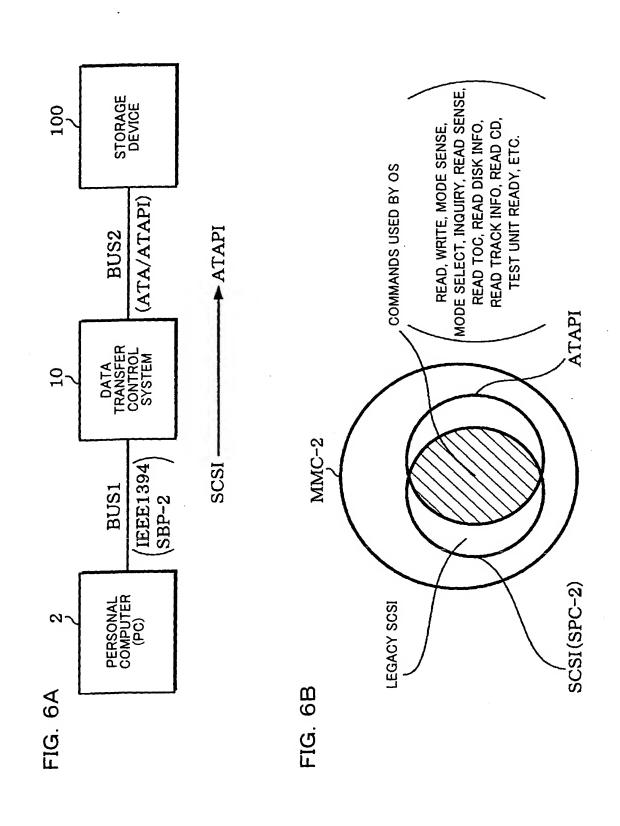
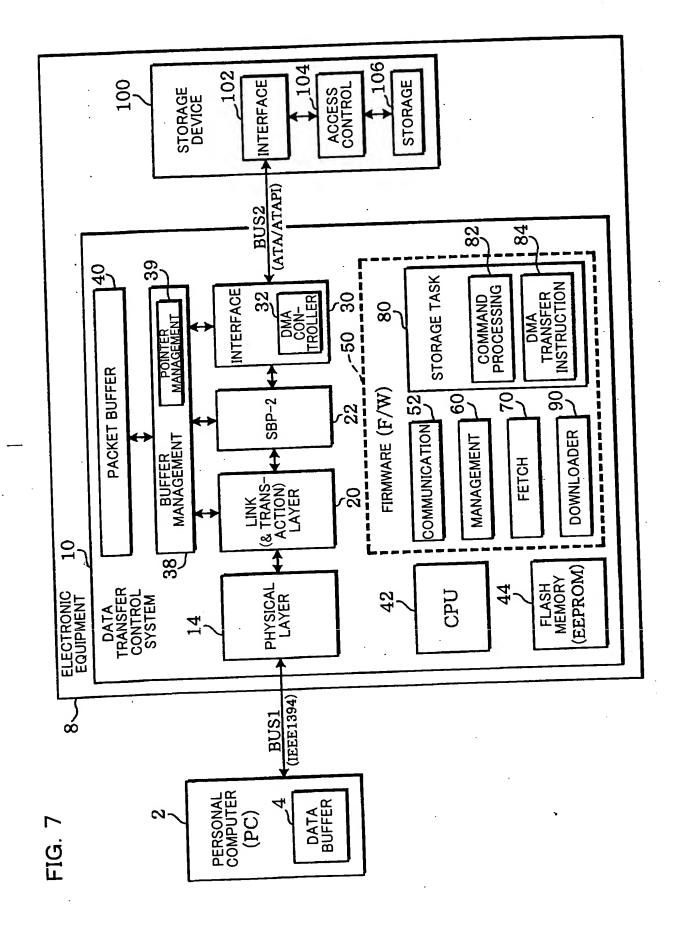


FIG. 5C PAGE TABLE IS NOT PRESENT







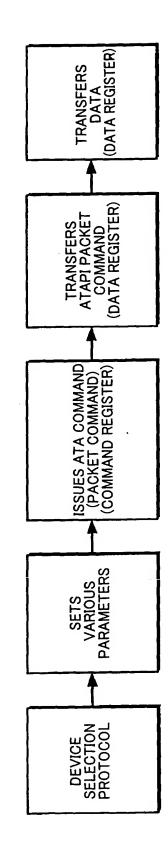


FIG. 9

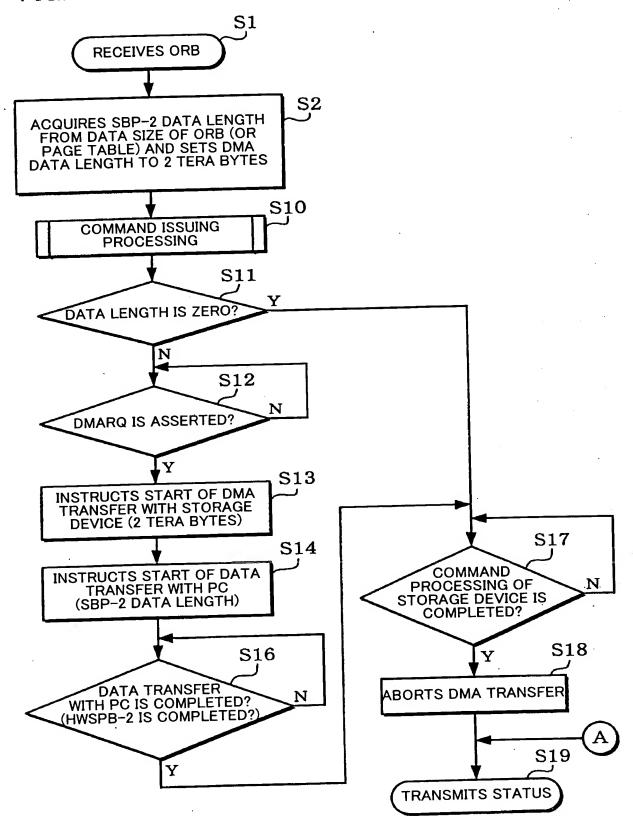


FIG. 10

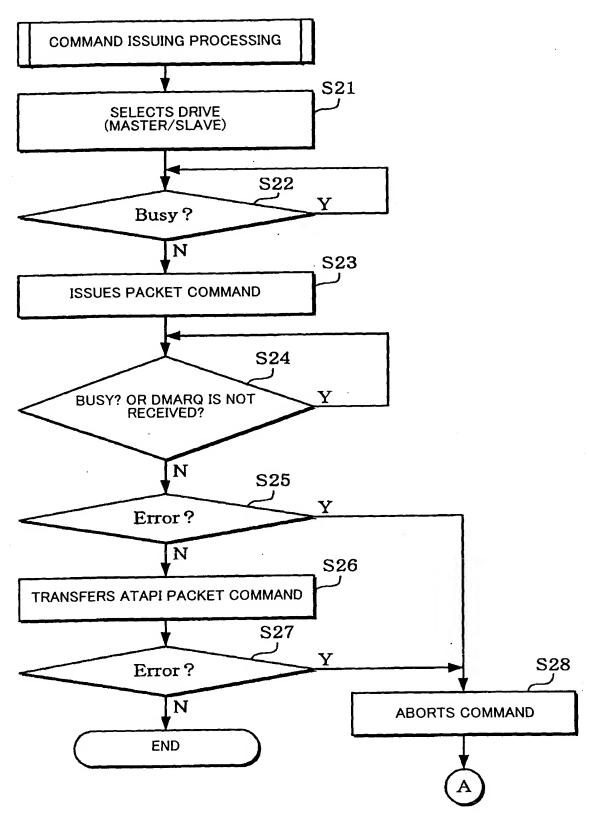
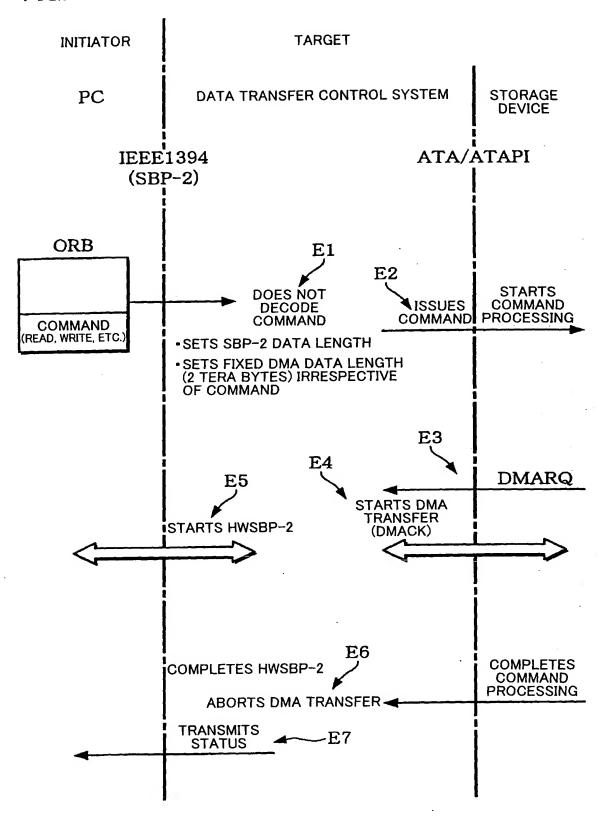


FIG. 11



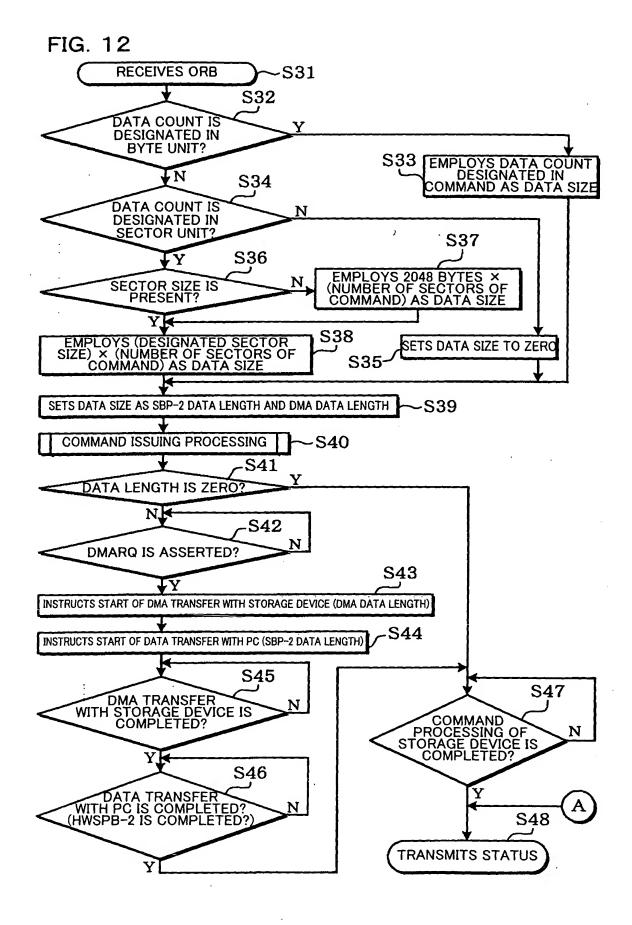


FIG. 13

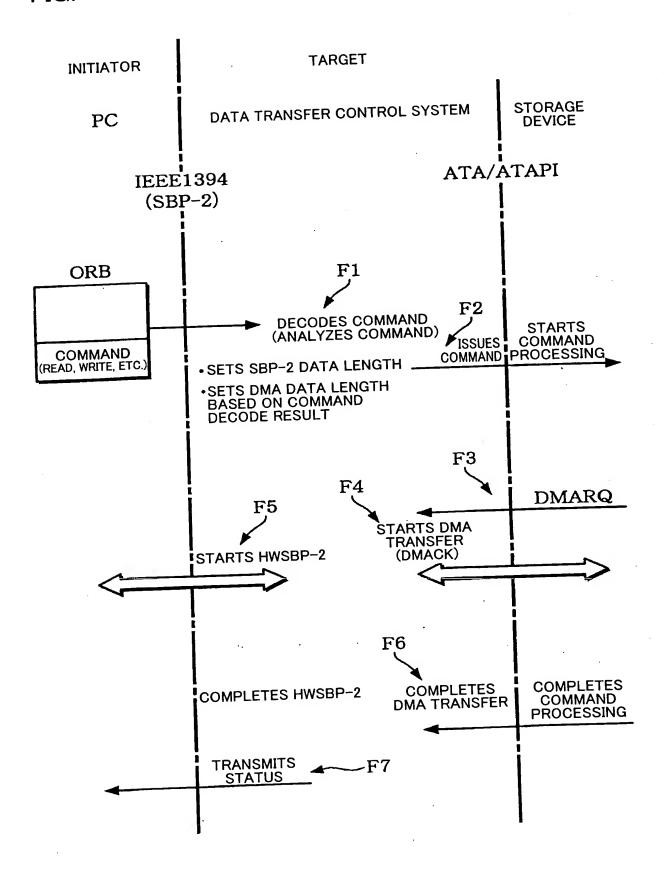
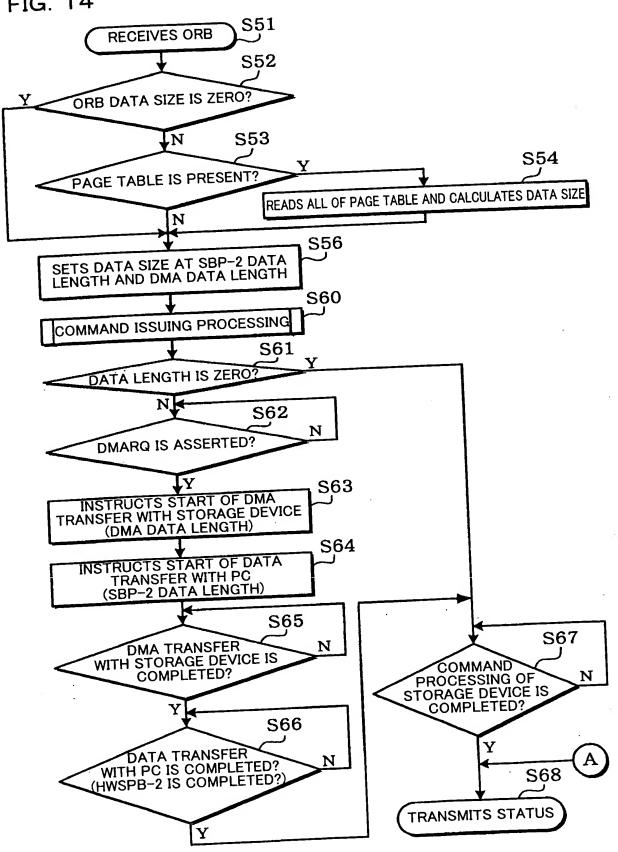


FIG. 14



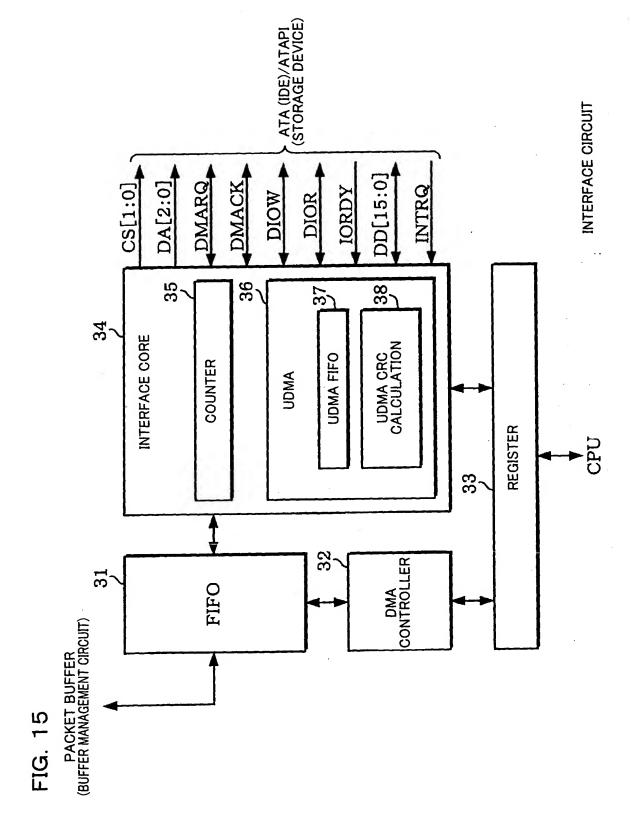


FIG. 16A
PIO READ (STORAGE DEVICE → DATA TRANSFER CONTROL SYSTEM → PC)

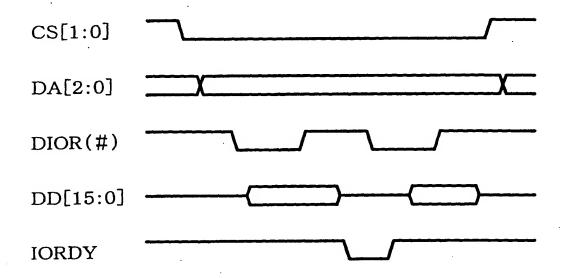


FIG. 16B
PIO WRITE (PC → DATA TRANSFER CONTROL SYSTEM → STORAGE DEVICE)

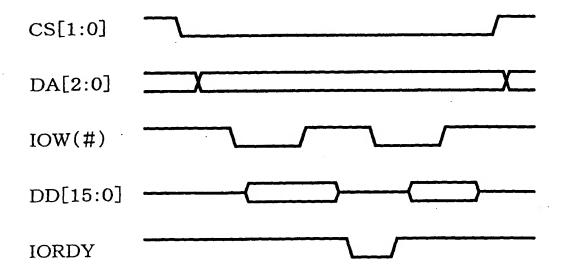


FIG. 17A DMA READ (STORAGE DEVICE ightarrow DATA TRANSFER CONTROL SYSTEM ightarrow PC) CS[1:0](#) DA[2:0] **DMARQ** DMACK(#) DIOR(#) DD[15:0] FIG. 17B DMA WRITE (PC ightarrow DATA TRANSFER CONTROL SYSTEM ightarrow STORAGE DEVICE) CS[1:0](#) DA[2:0] **DMARQ** DMACK(#) DIOW(#) DD[15:0]

FIG. 18A

UltraDMA READ (STORAGE DEVICE → DATA TRANSFER CONTROL SYSTEM → PC)

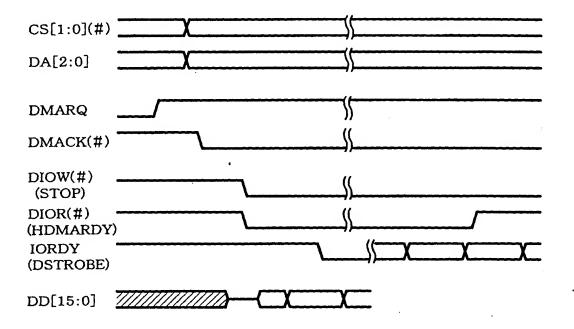


FIG. 18B

UltraDMA WRITE (PC → DATA TRANSFER CONTROL SYSTEM → STORAGE DEVICE)

